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Electron  
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PATENT APPLICATION  
Do. No. 8371-127

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Apostolos VOUTSAS and Yukihiro NAKATA

Serial No. 09/945,063

Examiner: George R. Fourson III

Filed: August 31, 2001

Group Art Unit: 2823

For: **METHOD OF FORMING MULTI-LAYERS FOR A THIN FILM  
TRANSISTOR (TFT) AND THE DEVICE FORMED THEREBY**

Box Non-Fee Amendment  
Assistant Commissioner for Patents  
Washington, D.C. 20231

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**RESPONSE TO RESTRICTION REQUIREMENT**

Responsive to the Restriction Requirement, dated October 31, 2002, applicant hereby elects Group I, claims 1-16, drawn to processes, classified in Class 438, subclass 763, without traverse.

The Examiner is encouraged to telephone the undersigned at (503) 222-3613 if it appears that an interview would be helpful in advancing the case.

Respectfully submitted,

Hosoon Lee  
Limited Recognition Under 37 CFR § 10.9(b)

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ON Dec. 2, 2002

*Judy Mcginnis*